



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE

United States Patent and Trademark Office

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/750,302	12/30/2003	Chih-Ping Hsu	030221	1672
23596 7590 10/21/2009 QUALCOMM INCORPORATED 5775 MOREHOUSE DR. SAN DIEGO, CA 92121				
EXAMINER GESESSE, TILAHUN				
ART UNIT 2618		PAPER NUMBER		
NOTIFICATION DATE 10/21/2009		DELIVERY MODE ELECTRONIC		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

us-docketing@qualcomm.com

kascanla@qualcomm.com

nanm@qualcomm.com

Application No.

Applicant(s)

10/750,302

HSU ET AL.

Response to Rule 312 Communication

Examiner

Art Unit

TILAHUN GESESSE

2618

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

1. ☒ The amendment filed on 09 October 2009 under 37 CFR 1.312 has been considered, and has been:

a) ☐ entered.

b) ☐ entered as directed to matters of form not affecting the scope of the invention.

c) ☐ disapproved because the amendment was filed after the payment of the issue fee.

Any amendment filed after the date the issue fee is paid must be accompanied by a petition under 37 CFR 1.313(c)(1) and the required fee to withdraw the application from issue.

d) ☒ disapproved. See explanation below.

e) ☐ entered in part. See explanation below.

Regarding claim 21, recites "a memory unit storing executable by a processor " and regarding claim 33 , recites "a momery unit storing codes which when executed by a processor" in this case, the memory unit should supported by the specification i.e., RAM or ROM etc. this raises the 101 flag.

the memory unit storing executable by a processor , it is not clear that what the memory unit is storing and processor is executing in the recited claims. it should include an instruction or program that has to be executed by the processor.

t

//Tilahun Gesesse//
Primary Examiner, Art Unit 2618